



PATENT APPLICATION
Docket No. 4591-170
Client No. IE10189-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jong-Ho Lee and Nae-In Lee

Serial No. 09/776,059 Examiner: Douglas Wille A

Filed: February 2, 2001 Art Unit: 2814

For: DIELECTRIC LAYER FOR SEMICONDUCTOR DEVICE
AND METHOD OF MANUFACTURING THE SAME

Confirmation No. 9369

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- ☒ Publication and Issue Fee
- ☒ In connection with issuance of a patent:
 - ☐ Supplemental Declaration ☒ PTO Form 85B
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
- ☒ A copy of the Limited Recognition Under 37 CFR § 10.9(b).
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee

Limited Recognition Under 37 CFR § 10.9(b)

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